

# MARATHWADA INSTITUTE OF TECHNOLOGY,CIDCO

## Question Bank

### Digital Electronics

#### Class:B.Sc(CS/IT)-I sem

Q1. What is Flip flop Explain JK Flip flop

Q2. What is Flip flop Explain SR Flip flop

Q3. What is Flip flop Explain Master-SlaveFlip flop

Q4. What is Flip flop Explain T Flip flop

Q5. Diff. between synchronous and asynchronous counters.

Q6. what is shift register? Draw circuit diagram for it.

Q7. Design the following

1.MOD 8 ripple Counter

Q8. Define Adder Explain Half adder with suitable diagram and truth table

Q9. Define Adder Explain Full adder with suitable diagram and truth table

Q10. Explain complementation and commutative law with suitable example

Q11. What is counter? Explain BCD Counter with suitable diagram

Q12. Define Subtractor Explain Half Subtractor with suitable diagram and truth table

Q13. Define Subtractor Explain Full Subtractor with suitable diagram and truth table

Q14. What is multiplexer explain 4:1 Multiplexer

Q15. What is De-multiplexer explain 4:1 Multiplexer

Q16. Explain Serial-In serial-out shift register

Q17. Explain Serial-In parallel-out shift register

Q18. Explain Parallel-In serial-out shift register

Q19. Explain Parallel-In parallel-out shift register

Q20 What is K-map Explain K map for 3 Variables

Q21. Explain Logic gates with Example.

**NOTE : In Unit 1 All points are imp in the exam point of view.**